

WHAT IS CLAIMED IS:

1. A method of electrically coupling a central processing unit (CPU) of a server to a plurality of interface modules comprising:
 - 5 routing an I/O bus having a first format from said central processing unit to primary sides of a plurality of bus adapter chips; and
 - 10 routing an I/O bus of said first format from secondary sides of said bus adapter chips to respective ones of said interface modules.
- 15 2. The method of Claim 1, wherein the bus adapter chips comprise electrical hardware which provides arbitrated access along the I/O busses.
- 20 3. The method of Claim 1, wherein routing the I/O buses to and from bus adapter chips comprising electrically isolating the CPU from electrical disruption when one of the interface modules is removed.
- 25 4. The method of Claim 1, further including mounting a plurality of interface cards in interface card slots in said plurality of interface modules.
- 30 5. The method of Claim 1, further including removably mounting said interface modules on a chassis.
6. The method of Claim 5, wherein removably mounting the interface module comprises mounting said interface module such that said interface module may be removed from said chassis and disconnected electrically from said CPU or mounted on said chassis and connected electrically to said CPU without powering down said CPU.
7. A method of electrically coupling a plurality of interface modules to a CPU such that at least one of the interface modules can be disconnected without powering down the remaining interface modules or the CPU, said method comprising:
 - 25 mounting a CPU on a chassis;
 - removably mounting a plurality of interface modules to said chassis;
 - 30 mounting a backplane printed circuit board on the chassis, wherein the backplane printed circuit board comprises at least one bus adapter chip for each of the plurality of interface modules, and wherein each bus adapter chip has a primary side and a secondary side, and wherein each bus adapter chip has electrical hardware that isolates the primary side from the secondary side when the corresponding interface module has been removed from the chassis;

routing an I/O bus on said backplane printed circuit board from the primary side of the at least one bus adapter chip to the CPU; and

5 routing an I/O bus on said backplane printed circuit board from the secondary side of the at least one bus adapter chip to the corresponding one of the interface modules.

8. The method of Claim 7, wherein the at least one bus adapter chip comprises electrical hardware providing arbitrated access and speed matching along the I/O busses.

10. The method of Claim 7, wherein the I/O busses comprise peripheral component interconnect (PCI) busses.

10. The method of Claim 9, wherein said act of mounting the plurality of interface modules to the backplane printed circuit board comprises the acts of:

15 connecting a 180 pin female connector on said backplane printed circuit board with a 180 pin male connector on a interface module of said plurality of interface modules; and

connecting a 360 pin female connector on said backplane printed circuit board with a 360 pin male connector on said CPU.

11. The method of Claim 7, further including mounting a plurality of interface cards in interface card slots in said plurality of interface modules.

20 12. The method of Claim 11, wherein said printed circuit board powers down said interface modules including said interface cards thereon.

13. A method of electrically coupling a plurality of interface modules to a CPU such that at least one of the interface modules can be removed without powering down the remaining interface modules or the CPU, said method comprising:

25 mounting a backplane printed circuit board on the back of a chassis;

connecting a CPU module to said backplane printed circuit board when mounting a CPU module on said chassis; and

removably mounting a plurality of interface modules to the backplane printed circuit board; and

30 connecting the plurality of interface modules to the backplane printed circuit board with bus adapter chips configured to proved arbitrated access to said

interface modules and electrical termination and isolation between the interface modules and the CPU module when a interface module is removed.

14. The method of Claim 13, where said act of connecting said interface module to said backplane printed circuit board comprises the act of connecting a high density connector of said interface module to a high density connector on said backplane printed circuit board.

5 15. The method of Claim 14, further including mounting a plurality of interface cards in interface card slots in said plurality of interface modules.

10 16. A system for electrically coupling a central processing unit (CPU) of a server to a plurality of interface modules, the system comprising:

means routing an I/O bus having a first format from said central processing unit to primary sides of a plurality of bus adapter chips; and

means for routing an I/O bus of said first format from secondary sides of said bus adapter chips to respective ones of said interface modules.

15 17. The system of Claim 16, wherein the bus adapter chips comprise electrical hardware which provides arbitrated access along the I/O busses.

18. The system of Claim 16, wherein the means for routing the I/O buses to and from bus adapter chips comprise means for electrically isolating the CPU from electrical disruption when one of the interface modules is removed.

20 19. The system of Claim 16, further including means for mounting a plurality of interface cards in interface card slots in said plurality of interface modules.

20. The system of Claim 16, further including means for removably mounting said interface modules on a chassis.